

AUG 10 2006

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TO: Attn.: Examiner Steve N. Nguyen FROM: Mr. James R. Foley, Reg. No. 39,979FAX NO: (571) 273-8300 FAX NO: (312) 704-8023*If you experience any difficulty with this transmission, please call (312) 704-1890 for assistance.***ORIGINAL COPY AND ENCLOSURES**

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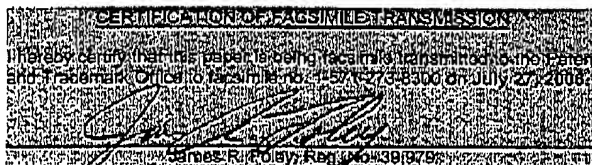
Inventors: Ghasi R. Agrawal et al.
For: A Novel BISR Mode to Test the
Redundant Elements and Regular
Functional Memory to Avoid Test
Escapes

Serial No.: 10/701,332

Art Unit: 2138

Filed: November 4, 2003

Attorney Ref.: 03-1343

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AUG 10 2006

Case No. 03-1343

FORM PTO-1083

In re application of: Ghasi R. Agrawal et al.

Serial No.: 10/701,332

Filed: November 4, 2003

Art Unit: 2138

For: A NOVEL BISR MODE TO TEST THE
REDUNDANT ELEMENTS AND REGULAR
FUNCTIONAL MEMORY TO AVOID TEST
ESCAPES

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P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is a Response to the Office Action mailed May 11, 2006.

The filing fee has been calculated as shown below:

	(Col. 1)		(Col. 2)	(Col. 3)
	Claims Remaining After Amendment		Highest No. Previously Paid for	Present Extra
TOTAL	* 12	MINUS	** 20	0
INDEP.	* 2	MINUS	** 3	0
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				

SMALL ENTITY

Rate	Addit. Fee
x 25 =	\$.00
x 100 =	\$.00
+ 180 =	\$.00
TOTAL ADDIT. FEE	\$.00

LARGE ENTITY

Rate	Addit. Fee
x 50 =	\$.00
x 200 =	\$.00
+ 360 =	\$.00
TOTAL	\$.00

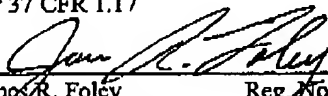
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OR

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- ☒ Any filing fees required under 37 CFR 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 CFR 1.17

Dated: August 10, 2006


James R. Foley Reg. No. 39,979
Attorney of Record

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SMALL ENTITY		OR	LARGE ENTITY	
Rate	Addit. Fee		Rate	Addit. Fee
x 25 =	\$.00		x 50 =	\$.00
x 100 =	\$.00		x 200 =	\$.00
+ 180 =	\$.00		+ 360 =	\$.00
TOTAL ADDIT. FEE	\$.00	OR	TOTAL	\$.00

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

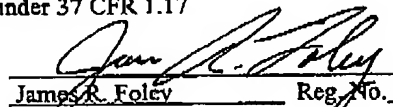
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Dated: August 10, 2006


James R. Foley
Attorney of Record

Reg. No. 39,979

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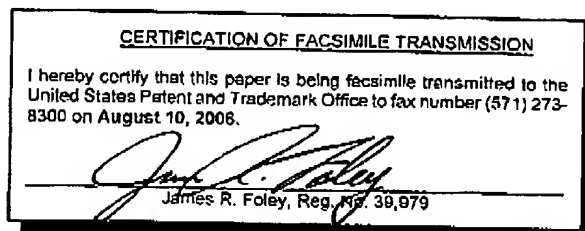
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PATENT
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/701,332
Filed: November 4, 2003
Title: **A Novel BISR Mode to Test
the Redundant Elements and
Regular Functional Memory
to Avoid Test Escapes**
Inventor: Ghasi R. Agrawal et al.
Art Unit: 2138
Examiner: Steve N. Nguyen
Atty. Ref: 03-1343

**RESPONSE TO THE OFFICE ACTION MAILED MAY 11, 2006**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application and in response to the Office Action mailed May 11, 2006, kindly enter the following amendments and consider the following remarks toward reconsideration of the present application.